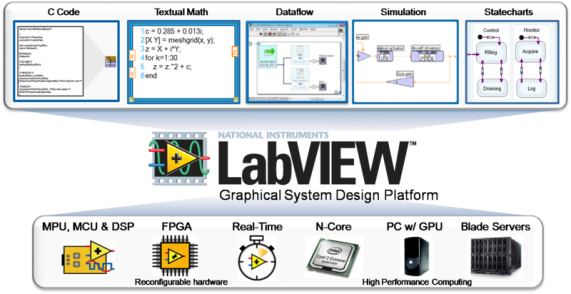
 UNIDAD PROFESIONAL INTERDISCIPLINARIA DE INGENERIA Y TECNOLOGIAS AVANZADAS

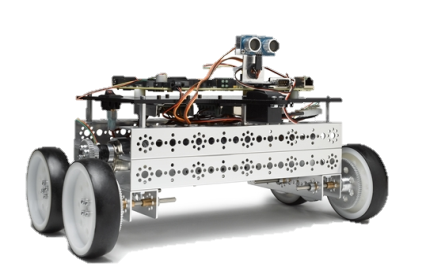
**“Práctica 1 Modularidad”**

Alumno: ZARAZUA AGUILAR LUIS FERNANDO

GRUPO: 3mm3

PROFESOR: Erick HuitrÓn Ramírez

MATERIA: instrumentación virtual





Panel Frontal de SubVI Perceptron

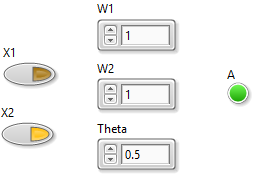
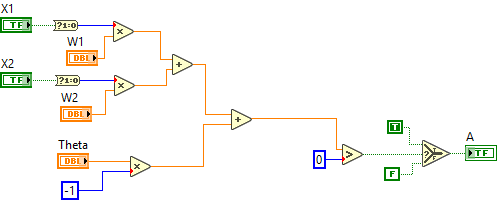
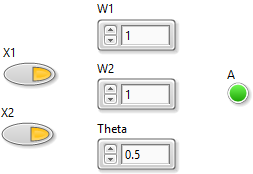
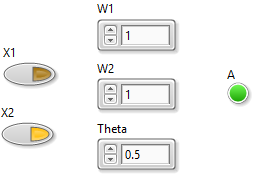
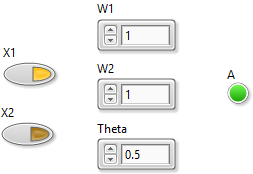
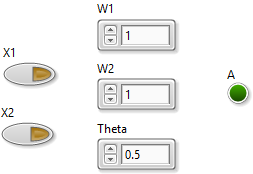


Diagrama de bloques de SubVI Perceptron

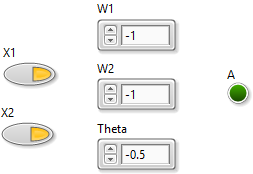
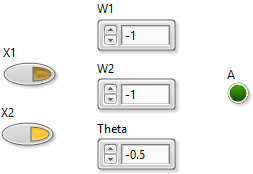
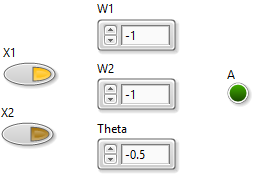
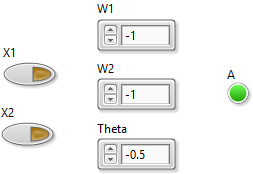


Resultados de SubVI Perceptron

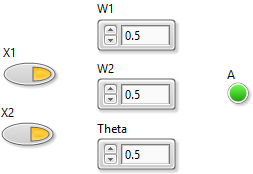
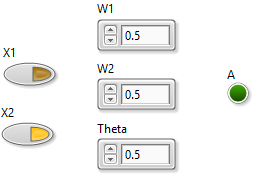
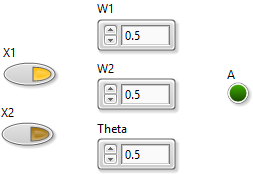
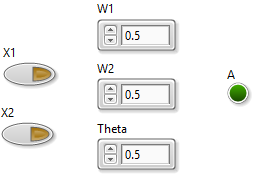
*Configuración para OR*



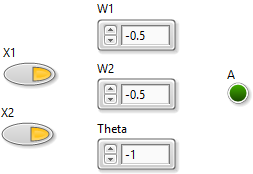
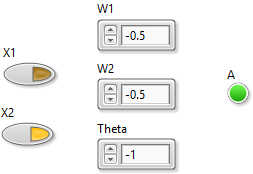
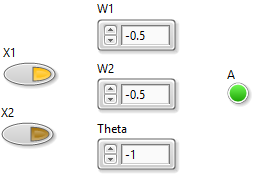
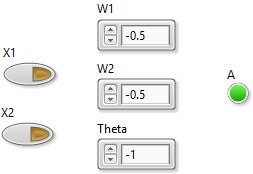
*Configuración para NOR*



*Configuración para AND*



*Configuración para NAND*



Panel Frontal de VI P1E1 ZAL 2015640414

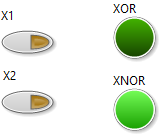
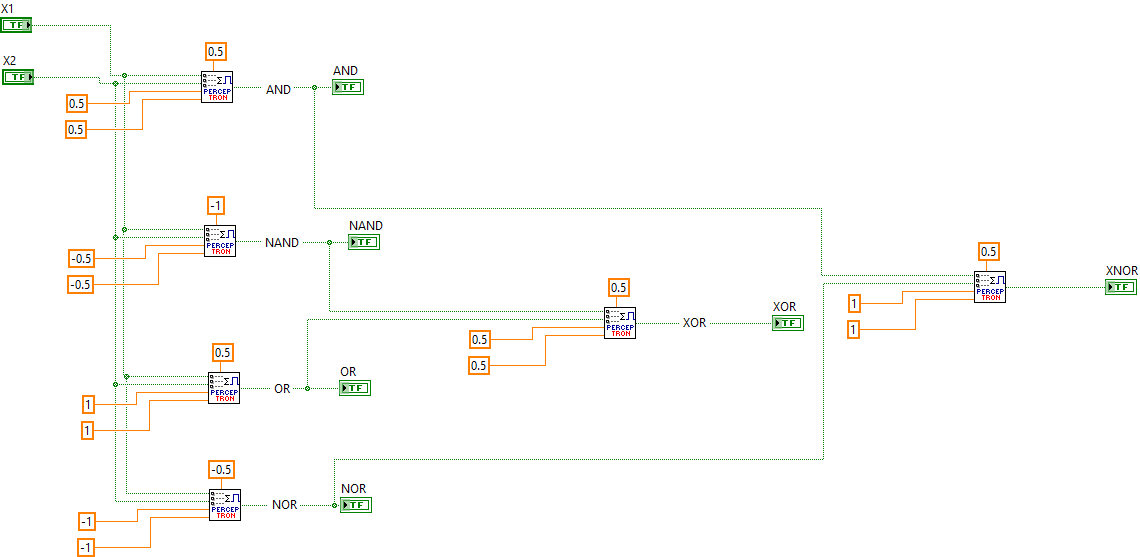
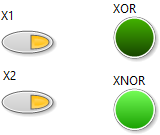
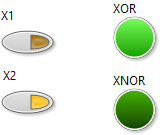
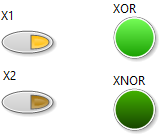
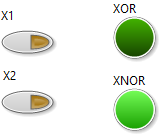
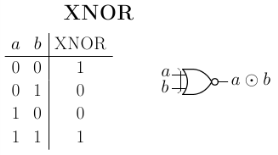
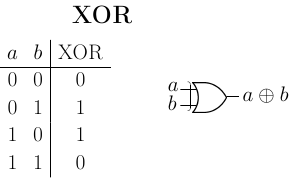


Diagrama de bloques de VI P1E1 ZAL 2015640414



Resultados





Panel Frontal de SubVI Decodificador\_7seg

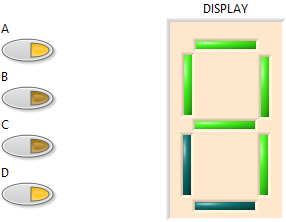
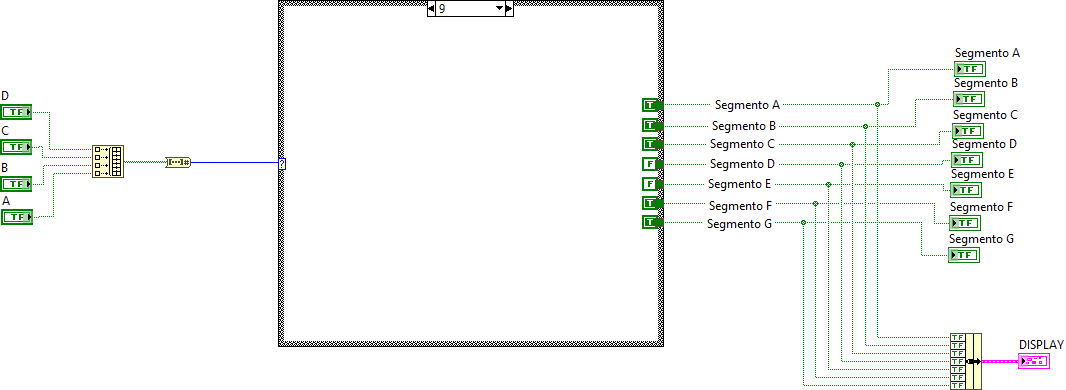
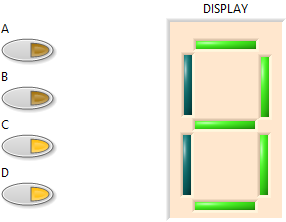
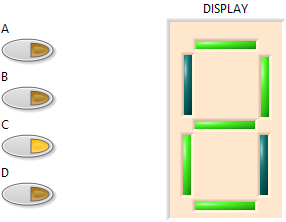
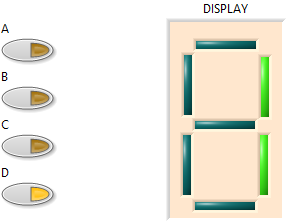
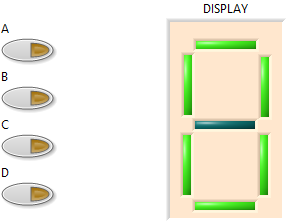
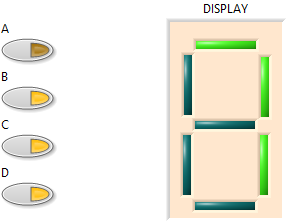
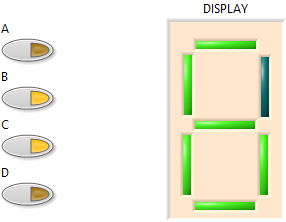
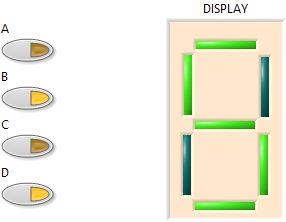
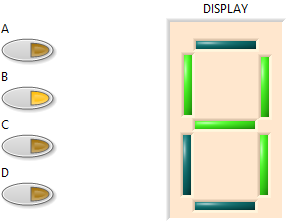


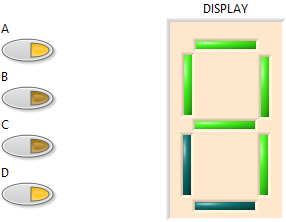
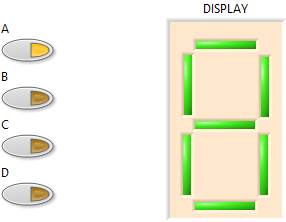
Diagrama de bloques de SubVI Decodificador\_7seg



Resultados







Panel Frontal de SubVI Conv\_BIN2BCD

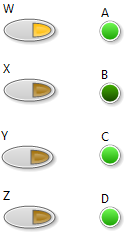
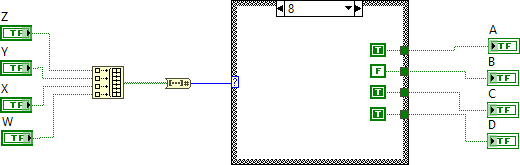
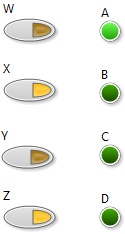
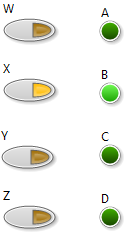
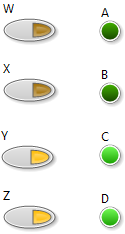
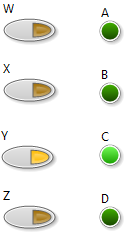
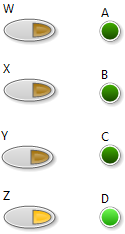
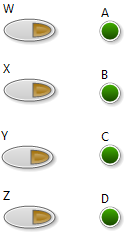


Diagrama de bloques de SubVI Conv\_BIN2BCD

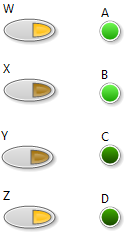
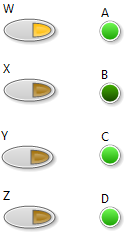
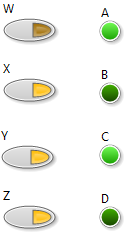
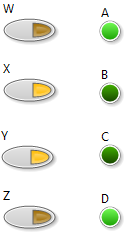


Resultados

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 0 | 0 | 2 | 2 | 4 | 4 | 6 | 6 | 8 | 8 | 10 | 16=0x10 |



|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| 12 | 0x12 | 14 | 0x14 | 16 | 0x16 | 18 | 0x18 |



Panel Frontal de SubVI P1E2 ZAL 2015640414

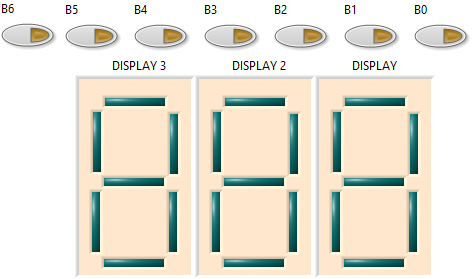
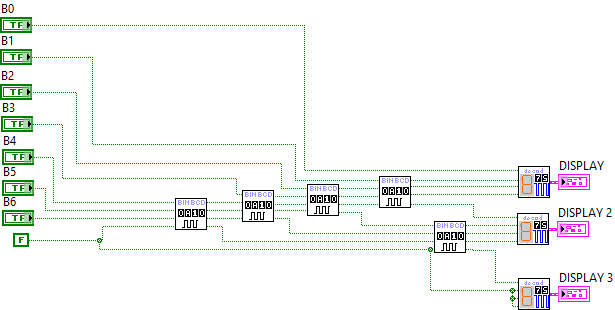


Diagrama de bloques de SubVI P1E2 ZAL 2015640414



Resultados

